

RESPONSE TO RESTRICTION REQUIREMENT

Serial Number: 10/732,712

Filing Date: December 11, 2003

Title: METHOD AND APPARATUS FOR MANUFACTURING A TRANSISTOR-OUTLINE (TO) CAN HAVING A CERAMIC HEADER

Assignee: Intel Corporation

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Dkt: 884.G23US1

REMARKS

This paper responds to the Restriction Requirement mailed on July 13, 2005.

Claims 1-20 are pending in this application.

Response to Restriction Requirement

In response to the Restriction Requirement mailed July 13, 2005, Applicant elects, without traverse, Species B, Subspecies H, and Subspecies D. This election includes claims 1, 2, 3, 4, 5, 6, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, and 20. Applicant reserves the right to reintroduce the non-elected claims in a divisional application at a later date.

The Examiner is invited to telephone Applicant's attorney, John Greaves at (801) 278-9171, or Applicant's below-named representative to facilitate the prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,
TIEYU ZHENG
By his Representatives,
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Date

December 13 2005

By

Charles E. Steffey

Charles E. Steffey
Reg. No. 25,179

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 13th day of December, 2005.

Name

Amy Moriarty

Signature

[Signature]